

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application of:

Shunpei YAMAZAKI et al.

Serial No. 10/712,062

Filed: November 14, 2003

For: SEMICONDUCTOR DEVICE AND

METHOD OF FABRICATING SAME

) Group Art Unit: 2826

) Examiner: Fetsum Abraham

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on October 11, 2006.

Adelle M. Stanger

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing information known to Applicant and requests that this information be made of record in the above identified application. Copies are submitted herewith in accordance with 37 C.F.R. 1.98(a).

Copies of U.S. patents and U.S. publications are not enclosed in accordance with the Notice published in the Official Gazette on August 5, 2003 entitled *Information Disclosure Statements May Be Filed Without Copies of U.S. Patents and Published Applications in Patent Applications filed after June 30, 2003*, which waives the requirement under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. publication.

This Information Disclosure Statement is being submitted with an RCE. Therefore, no fee is required.

Respectfully submitted,

Eric J. Robinson

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.